

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Slemmons, et al.  
Appl No.: To Be Assigned  
Filed: Concurrently Herewith  
For: MICROBEAM ASSEMBLY FOR INTEGRATED  
CIRCUIT INTERCONNECTION TO SUBSTRATES



BOX DIVISIONAL PATENT APPLICATION  
U.S. Patent and Trademark Office  
P. O. Box 2327  
Arlington, VA 22202

December 5, 2001

INFORMATION DISCLOSURE STATEMENT  
CITATION UNDER 37 C.F.R. § 1.97

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#16  
3-6-02

Sir:

Attached is a list of documents on form PTO-1449. It is requested that the Examiner consider these documents and officially make them of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. By submitting the listed documents, Applicant in no way makes any admission as to the prior art status of the listed documents, but is instead submitting the listed documents for the sake of full disclosure.

No items are attached since they were supplied in parent Application No. 09/383,720 filed August 26, 1999. Since the benefit of this application was claimed under 35 U.S.C. 120, no copies need to be furnished in accordance with 37 C.F.R. 1.98(d); however, copies will be furnished on request.

Respectfully submitted,

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